

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,876,593 B2
DATED : April 5, 2005
INVENTOR(S) : Shi et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,


Line 67, delete "10" and insert -- 110 --.

Column 11,

Line 7, insert -- During normal operation of computer system 700, CPU 710 executes instructions causing CPU 710 to write marking data into marking buffer 745 to mark a row within a memory array, such as memory array 751a of memory device 750a, as not having data to be preserved. As a result, when memory controller 740 is to command that a row within memory array 751a be refreshed, memory controller 740 will obtain marking data from marking buffer 745 to identify a row marked as having data to be preserved, and the identified row will be a row other than the one that CPU 710 was just caused to mark as not having data to be preserved. Memory controller 740 will then transmit a row address identifying that other row with the refresh command to memory device 750a, causing memory device 750a to carry out a refresh operation on that specific row within memory array 751a. --.

Signed and Sealed this

Twenty-third Day of August, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office